



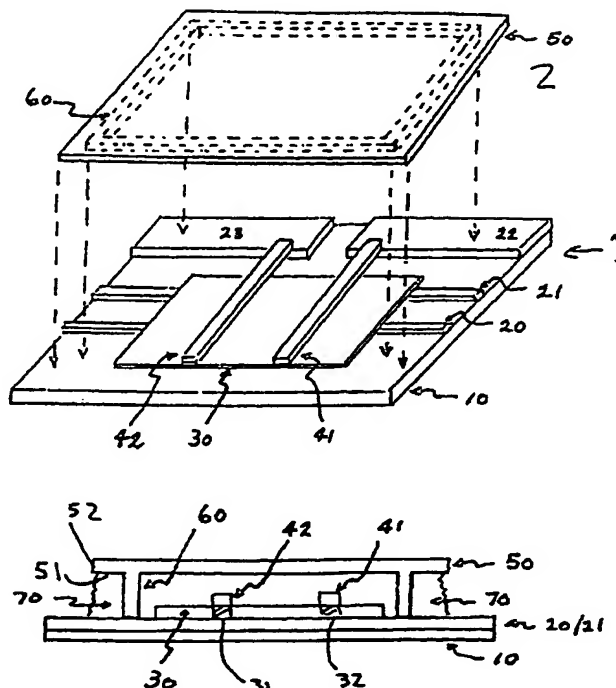
INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(51) International Patent Classification 7 : H01L 51/20	A1	(11) International Publication Number: WO 00/69002 (43) International Publication Date: 16 November 2000 (16.11.00)
(21) International Application Number: PCT/US00/10392 (22) International Filing Date: 18 April 2000 (18.04.00) (30) Priority Data: 09/309,846 11 May 1999 (11.05.99) US (71) Applicant: THE DOW CHEMICAL COMPANY [US/US]; 2030 Dow Center, Midland, MI 48674 (US). (72) Inventors: BERNIUS, Mark, T.; 401 Mayfield Lane, Midland, MI 48640 (US). WOO, Edmund, P.; 300 Mayfield Lane, Midland, MI 48640 (US). (74) Agent: ZERULL, Susan, Moeller, Intellectual Property, P.O. Box 1967, Midland, MI 48641-1967 (US).	(81) Designated States: JP, KR, SG, European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE). Published <i>With international search report.</i> <i>Before the expiration of the time limit for amending the claims and to be republished in the event of the receipt of amendments.</i>	

(54) Title: AN ELECTROLUMINESCENT OR PHOTOCELL DEVICE HAVING PROTECTIVE PACKAGING

(57) Abstract

The device of this invention comprises an optoelectronic element (1), a cover (2), and an adhesive (70). The optoelectronic element comprises a substrate (10) bearing an anode (20, 21), a film comprising an optoelectronic material (30), and a cathode (41, 42). The cover comprises a lid (50) and a raised rim (60) which is recessed from the outer edge of the lid. The cover (2) contacts the element (1) so that the film comprising the optoelectronic material (30) is located between the substrate and the lid, and within the region defined by the raised rim (60). An adhesive (70), located in a channel defined by the bottom surface of the lid from the outer edge to the rim, the rim, and the top surface of the optoelectronic element outside of the region defined by the rim, serves to attach the cover to the optoelectronic element.



FOR THE PURPOSES OF INFORMATION ONLY

Codes used to identify States party to the PCT on the front pages of pamphlets publishing international applications under the PCT.

AL	Albania	ES	Spain	LS	Lesotho	SI	Slovenia
AM	Armenia	FI	Finland	LT	Lithuania	SK	Slovakia
AT	Austria	FR	France	LU	Luxembourg	SN	Senegal
AU	Australia	GA	Gabon	LV	Latvia	SZ	Swaziland
AZ	Azerbaijan	GB	United Kingdom	MC	Monaco	TD	Chad
BA	Bosnia and Herzegovina	GE	Georgia	MD	Republic of Moldova	TG	Togo
BB	Barbados	GH	Ghana	MG	Madagascar	TJ	Tajikistan
BE	Belgium	GN	Guinea	MK	The former Yugoslav Republic of Macedonia	TM	Turkmenistan
BF	Burkina Faso	GR	Greece	ML	Mali	TR	Turkey
BG	Bulgaria	HU	Hungary	MN	Mongolia	TT	Trinidad and Tobago
BJ	Benin	IE	Ireland	MR	Mauritania	UA	Ukraine
BR	Brazil	IL	Israel	MW	Malawi	UG	Uganda
BY	Belarus	IS	Iceland	MX	Mexico	US	United States of America
CA	Canada	IT	Italy	NE	Niger	UZ	Uzbekistan
CF	Central African Republic	JP	Japan	NL	Netherlands	VN	Viet Nam
CG	Congo	KE	Kenya	NO	Norway	YU	Yugoslavia
CH	Switzerland	KG	Kyrgyzstan	NZ	New Zealand	ZW	Zimbabwe
CI	Côte d'Ivoire	KP	Democratic People's Republic of Korea	PL	Poland		
CM	Cameroon	KR	Republic of Korea	PT	Portugal		
CN	China	KZ	Kazakstan	RO	Romania		
CU	Cuba	LC	Saint Lucia	RU	Russian Federation		
CZ	Czech Republic	LJ	Liechtenstein	SD	Sudan		
DE	Germany	LK	Sri Lanka	SE	Sweden		
DK	Denmark	LR	Liberia	SG	Singapore		
EE	Estonia						

AN ELECTROLUMINESCENT OR PHOTOCELL DEVICE
HAVING PROTECTIVE PACKAGING

This application is under a Government contract with The Department of
5 Commerce Naval Air Warfare Center Program, Project Number N00421-98-3-1187.

This invention relates to devices having an anode, a cathode, and an
optoelectronic film between the anode and the cathode. The invention especially relates to
such devices where the optoelectronic film comprises an organic optoelectronic material.

Optoelectronic devices such as photocells (for example, photodetectors,
10 photodiodes, photovoltaics) and electroluminescent (EL) elements may be formed by
sandwiching films comprising optoelectronic materials between electrodes. When an EL
device is subjected to an applied voltage, holes injected from the anode and electrons
injected from the cathode will combine in the optoelectronic material to form singlet excitons,
which can undergo radiative decay, thereby liberating light. Conversely, in photocells, light
15 that is incident upon the optoelectronic material is converted into electric current.

Optoelectronic devices have been made with certain inorganic and organic
semi-conductors as the optoelectronic materials. In addition, the film comprising the
optoelectronic material may comprise a plurality of layers based on materials of the desired
properties. Constituent organic optoelectronic materials may be polymeric, as described by
20 Kraft and coworkers in Angew. Chem. Int. Ed., Vol. 37, pp. 402-428, (1998), or monomeric,
as described by Tang and VanSlyke in U.S. Patent 4,885,211 and by Tang in Information
Display, pp. 16-19, October, 1996. Other suitable materials include those disclosed in U.S.
Patents 5,708,130; and 5,728,801; PCT Applications WO 97/33193 and WO 00/06665; and
U.S. Patent Application, Serial No. 09/289,344.

25 The anode is typically a transparent or semi-transparent conducting material,
deposited on a transparent substrate, such as glass, so that light can escape from the EL
element or so that the optoelectronic film can be exposed to light. Indium-tin-oxide (ITO) is
generally the preferred anode material because of its excellent optical transparency and
electrical conductivity. Because most organic materials have low electron affinity, efficient
30 injection of electrons into them from the cathode is only possible when the cathode is a
metal of low work function, which can be deposited as pin-hole-free films by evaporation in
high vacuum or by sputtering. Preferred metals are lithium, calcium and magnesium, as well
as their alloys and blends with metals of higher work function. The use of low work function
metals in EL elements leads to higher EL efficiency but also environmental instability, as
35 these metals are known to be extremely sensitive to oxygen and moisture in ambient air.

Indeed, EL elements with calcium cathodes have been reported to lose 90 percent of their efficiency in 37 seconds in a highly humid environment according to Sheats, et al. in Science, Vol. 273, (1996), pp. 884-888. Magnesium is sometimes seen as a compromise choice, yet its stability in EL elements still leaves much to be desired, as Tang and VanSlyke (U.S. Patent 4,885,211) had shown that efficiency of these elements may drop by more than an order of magnitude in a matter of hours when exposed to an ambience, with a relative humidity of 20 percent or higher due to cathode corrosion.

In addition to the cathodes, polymer films in EL or photocell devices must also be protected from ambient oxygen and moisture as the injection of charge carriers generates highly sensitive chemical species: radical anions formed by injection of electrons and radical cations formed by injection of holes into the polymer film are readily destroyed by oxygen and water.

There is clearly a need for an effective packaging scheme to protect EL elements and photocell elements from ambient environment if they are to be used in commercial displays. This need has been recognized and several packaging approaches have been reported.

WO97/46052 teaches the use of a sheet of low melting metal alloys bonded onto the cathode of an EL element. Since the alloy layer is in direct contact with the cathode and, indeed, serves as the wiring contact, this approach is not suitable for EL elements with patterned cathodes, which is the preferred method for creating dot-matrix displays.

Another approach (see EP 777,281, and WO 97/16053) involves coating the cathode first with an organic film which is, in turn, coated with layers of metals, metal oxides, inorganic oxides, or, inorganic fluorides and the like. The problem with this approach is the application of the organic coating onto the cathode. Once an EL element is formed, no part of it may be exposed to moisture, organic solvent, oxygen, and elevated temperatures without causing damage. Thus, the application of an organic coating to a formed EL element, possible in principle, would be extremely difficult to accomplish without damaging the EL element in some way.

Yet another approach described in JP 7014675 involves co-forming films which are mixtures of inorganic fluorides and oxides and plasma-polymerized poly-*p*-xylylene. The inventor acknowledged the inferior barrier (to oxygen and moisture) properties of the polymer vs inorganic materials because of the presence of macro defects. Therefore, diluting the beneficial barrier properties of the inorganic materials with poly-*p*-xylylene can

hardly be an advantage. Furthermore, organic emitting materials are readily damaged by intense ultraviolet light inherent to the plasma generation process. Even if this packaging approach could protect the cathodes from oxygen and moisture in an ambient environment, it is likely to cause irreversible damage to the optoelectronic properties of the organic material.

A fourth approach involves sealing the flange of a metal or glass lid with a UV-curable adhesive onto the glass substrate of the EL element in vacuum or in an atmosphere of very dry nitrogen, as described by Nakada and Tohma in Display Devices, 1998, pp. 29-32. The dimensions of the lid are chosen such that there is a gap between the inner surface of the lid and the cathode. The adhesive film must provide adhesion between the cover and element and barrier to the ingress of moisture and oxygen. Since adhesion and barrier properties result from different chemical designs, it's unlikely for an adhesive to perform well in both functions. Material selection would be a compromise. It is also critical that the adhesive film be free of voids and pinholes and the adhesive be free of volatile organic compounds, dissolved gas, and moisture, which would be otherwise trapped in the sealed compartment and will eventually cause device deterioration.

It is clear that all of the known approaches have limitations. It is the object of this invention to provide a simple, yet effective, packaging scheme free of said limitations.

Figure 1A is a drawing of a representative cover with a raised rim for encapsulating optoelectronic elements; 1B is a cross-sectional view of the representative cover and raised rim.

Figure 2 shows the positioning of a representative cover relative to a representative optoelectronic element.

Figure 3 is a cross-section view of Figure 2 after the cover is attached to the optoelectronic element and sealed with an adhesive.

Figure 4 is a drawing of a glass sheet used in making a representative cover.

The device of this invention comprises a optoelectronic element, a cover, and an adhesive. The optoelectronic element comprises a substrate bearing an anode, a film comprising an optoelectronic material, and a cathode. The cover comprises a lid and a raised rim, which is recessed from the outer edge of the lid. The cover contacts the element so that the film comprising the optoelectronic material is located between the substrate and the lid, and within the region defined by the raised rim. An adhesive, located in a channel defined by the bottom surface of the lid from the outer edge to the rim, the rim, and the top

surface of the optoelectronic element outside of the region defined by the rim, serves to attach the cover to the optoelectronic element.

"Optoelectronic material" as used herein means a material, which is capable of converting electrical charge to light and vice versa, and, optionally, whose conductivity is enhanced by exposure to light.

"Optoelectronic element" as used herein means an electroluminescent device, a photocell, or similar devices.

The invention is further described with reference to the figures. Drawings are not scaled to true dimensions, some of which are exaggerated for purpose of clarity.

Figure 1A shows a representative cover 2 having a lid 50 and a raised rim 60 extending from the bottom surface 51 of the lid 50. The rim 60 is recessed from the outer edge 52 of the lid 50.

The optoelectronic element 1 shown in Figures 2 and 3 is a 4-pixel electroluminescent element. The element 1 comprises a substrate 10, two strips of anode 20 and 21 on the substrate, optoelectronic material 30, which is electroluminescent and is coated over the anode 20, 21, and the substrate 10, two strips of cathode 41 and 42 over the optoelectronic material 30, the cathode strips are in contact with conducting pads 22 and 23. Alternative means of contacting the anode and cathode to the optoelectronic material may be used. For example, a continuous layer of anode material could be covered by an image-wise distribution of an insulating material, such as a photoresist. The optoelectronic material would then be applied over the anode and the imaged insulating layer. Such a system would provide a luminescent image negative to the image of the insulating material. In addition, other supplementary layers, which are known in the art, such as barrier or protective layers, may also be used.

In an electroluminescent device, the conducting pads 22, 23 and the anodes 20, 21 are connected to a voltage source in such a way that the anode is positive relative to the conducting pads. The element then becomes electroluminescent in the area of the optoelectronic material defined by the intersections 31 and 32 of the anodes 20 and 21 with the cathodes 41 and 42. In a photodiode or photodetector, the conducting pads 22, 23 and the anodes 20, 21 are connected to a current collector, a current detector or another device run by the generated electricity.

The substrate 10 for the optoelectronic element 1 is a rigid, and, preferably, transparent material, such as glass or quartz. At least one of the substrate 10 or the lid 50

should be transparent so that light can enter or exit the device. The anode 20, 21 is preferably a transparent or semi-transparent material as is known in the art, such as indium tin oxide (ITO). The thickness of the anode is preferably less than about 1 micron, more preferably less than 0.5 micron, and most preferably less than 0.25 micron. The conducting pads may comprise any suitable conductive materials, such as an inert metal (for example, gold) or the material used in the anode (for example, ITO). The conductive pads preferably have a thickness less than about 1 micron, more preferably less than 0.5 micron and most preferably less than 0.25 micron.

The cathode 41, 42 is typically a metal or metal alloy having a low work function. Preferred materials include lithium, calcium, magnesium, alloys and blends of such metals with metals of higher work function. Preferably, prior to attachment of the cover, a thin, electrically insulating inorganic film is applied over the cathodes by either thermal evaporation or sputtering. Materials for this insulating film may be selected from among oxides of Ca, Mg, Si, Ge, and Mo, fluorides of Li, Ca or Mg, and nitrides of Si, and Ge. Preferably film thickness is no greater than 0.05 mm (millimeter) and no less than 0.001 mm with the provision that there is maintained a space between the inner surface of the lid and the surface of this film.

The optoelectronic material is preferably an organic charge carrier transporting material. For electroluminescent devices these materials must emit light and for photodiodes these materials must be capable of generating or carrying current when exposed to light. These materials are typically highly conjugated materials and may be monomeric or polymeric. Suitable monomeric materials include, for example, tertiary aromatic amines, metal complexes of 8-hydroxyquinoline, diarylbutadienes, stilbenes, as disclosed in U.S. Patent 4,769,292 (Tang, et al.) and Tang, Information Display, October (1996), pp. 16-19. Examples of suitable polymeric materials include polyarylene vinylenes and polyfluorenes. See, for example, Kraft, et al., Angew. Chem. Int. Ed. Vol. 37, pp. 402-428 (1998); U.S. Patents 5,708,130; 4,885,211 and 5,728,801; PCT Applications WO 97/33193 and WO 00/06665; and U.S. Patent Application, Serial No. 09/289,344 (Attorney Docket No. 44478A). The film comprising the optoelectronic material may also comprise other materials blended with the optoelectronic material as may be known in the art, such as stabilizers, adhesion promoters, fillers, and matrix materials. Alternatively, the film may comprise more than one layer of various materials, which may be other optoelectronic materials or non-optoelectronic materials, such as those which provide barrier or protective

properties. Other materials or layers added to the film should be chosen so as not to unduly inhibit the light emission or current generating properties of the film.

In the preferred embodiment, the rigid transparent substrate 10 bearing at least one transparent anode 20 is coated with the optoelectronic material or its precursor.

5 After forming the layer of the optoelectronic material, the cathode is deposited as a pin-hole-free film by evaporation in high vacuum or by sputtering.

The cover 2 is placed on the optoelectronic element 1 so that the rim 60 contacts the top surface(s) of the element 1. Referring to Figures 3 and 1B, the height, b, of the rim 60 is preferably such that there is no contact between the top surface of the
10 optoelectronic element 1 and the lid 50. Such a gap is desirable to protect the surface of the element from damage. The gap preferably has a thickness of at least 1 micron, more preferably at least 2 microns, and most preferably at least 5 microns. The cathodes 41 and 42, which are typically located at the top of the element, are particularly fragile. The height, b, of the rim 60 is preferably no greater than 3 mm and no less than 0.05 mm for most
15 optoelectronic elements. The width, a, of the rim 60 is preferably no greater than 5 mm and no less than 0.1 mm. The rim 60 is preferably recessed from the outer edge of the lid by a distance, c, no greater than 5 mm and no less than 0.5 mm.

The lid should preferably be fabricated from a rigid material with high barrier properties to oxygen and moisture. Examples of suitable materials include, for example,
20 glass, quartz, ceramic, aluminum, and stainless steel. Commercially available plastics may also be selected if the thus fabricated lid is given a barrier coating prior to use. Barrier coating materials include oxides of aluminum and silicon, and nitrides of silicon and germanium. The lid need not be electrically insulating. However, the rim, or portions of the rim which contact the optoelectronic element, should be electrically insulating. The rim may
25 be of the same or different material from the lid. Again, high barrier properties to oxygen and moisture are desired. These barrier properties provide one of the benefits of this invention, which is improved oxygen and moisture barrier, compared with a lid with no rim, which would simply be attached with an adhesive having relatively poor barrier properties. The rim also prevents the adhesive from seeping into the internal portions of the device, which could
30 seriously damage the device. For example, silicon nitride, a high barrier electrically insulating material, may be deposited onto a sheet of the lid material by sputtering through a mask to form the rim. Alternatively, the rim may be formed by physical or chemical etching of the lid material, such as glass, or the rim may be formed by stamping or machining from a metal sheet, followed by covering the surface of the rim with a layer of electrically insulating

film, such as aluminum oxide, silicon dioxide, silicon nitride, and the like. The thickness of the lid may vary with material choice. However, the thickness should be such to provide resistance to flexing during handling.

The cover is positioned in a manner to cover the optoelectronic material, while
5 leaving portions of at least some of the conductive materials exposed for connections to a power source (for an electroluminescent device) or a current detector or collector (for photodetectors and photodiodes). Preferably, the cover also completely covers the relatively fragile cathodes. It may be noted from Figures 2 and 3 that there is a very small gap
10 between the rim 60 and the substrate 10 corresponding to the thickness of the bonding pads 22, 23 and/or the anode 20, 21. This gap, however, is small enough that there is negligible effect on the barrier properties of the packaging of the device as a whole. Moreover, the adhesive, while not having excellent oxygen and moisture barrier properties, does provide some additional protection.

The adhesive is advantageously free or substantially free of volatile organic
15 components and, preferably, cures at or near room temperatures. UV-curable adhesives and two-component epoxy adhesives having these characteristics would be suitable. The adhesive may be applied by a syringe manually or by an automated dispenser equipped with a fine nozzle, as in many of the commercially available adhesive dispensers, after the cover is put in place.

20 In one preferred embodiment, the inner surface of the lid is coated with a thin film of reactive metal which serves as a sacrificial "getter" of traces of moisture, oxygen, and other potential harmful contaminants trapped inside the sealed cavity. The getter film is preferably fabricated by thermal evaporation or sputtering, preferably of calcium, barium, magnesium, and the like. Since the amount of trapped contaminants should be very low if
25 the sealing is conducted in a dry, inert atmosphere, film thickness need not exceed 0.5 mm with the provision that there is maintained a space between the cathodes and the getter film.

Example

This example illustrates the packaging of an EL element whose active area dimensions, defined as the area of the cathode, are approximately 58 mm by 40 mm
30 constructed on a piece of ITO-glass of the dimension 75 mm by 50 mm. The ITO-glass was coated with a photoresist material, which then image-wise was exposed to light and developed to provide an image of an insulating material over the ITO. A layer of organic, polymeric optoelectronic material was then applied over the imaged photoresist and ITO. A

35 nm (nanometer) calcium cathode over-coated with 270 nm of aluminum was then applied by vacuum evaporation.

5 The lid was fabricated in the following manner. A rectangular gasket (see Figure 4) made from an elastic film with outside dimensions of 70 mm by 46 mm and an uniform width of about 1.5 mm was applied onto a piece of glass 73 mm by 50 mm, such that the outer edges of the gasket were uniformly recessed from the perimeter of the glass sheet, as shown in Figure 4.

10 The side of the glass sheet with the gasket was subjected to sand-blasting until the thickness was reduced to about 1.4 mm from the original 1.8 mm. The area covered by the elastic film gasket was not etched by the sandblasting and effectively became the raised rim. The gasket was peeled off and the now completed lid was cleaned and dried.

15 A 100 nm film of barium was vacuum deposited onto the inside cavity formed by the raised rim. This serves as the "getter." In an inert atmosphere, the EL element and the lid were then positioned together as shown in Figures 2 and 3. While clamped firmly to maintain the alignment of the lid and the EL element, an UV adhesive (Nordland Optical Adhesive 88 from Nordland Corp., New Brunswick, N.J.) was applied by a syringe in the manner already described. The adhesive was cured by UV light for 3 seconds from a light source Dymax Light Welder PC3, 2.5 watt/cm², 320-390 nm from Dymax Corp., Torrington, CT. The packaged device can be operated and/or stored in ambient air for long periods of time (at least several weeks) without discernable degradation of luminance, as determined by visual inspection. The device was still functioning at the time of filing of this application.

CLAIMS

1. A device comprising:

5 (a) an optoelectronic element comprising (i) a substrate, (ii) a film comprising an optoelectronic material on a side of the substrate, and (iii) an anode and a cathode, each of which are in contact with the film, comprising the optoelectronic material and are separated from each other, wherein the side of the substrate bearing the film comprising the optoelectronic material, the film comprising optoelectronic material, the anode and the cathode define a top surface of the optoelectronic element,

10 (b) a cover comprising (i) a lid having a bottom surface, and an outer edge, and (ii) a rim extending from the bottom surface of the lid, recessed from the outer edge of the lid, and defining a region circumscribed by the rim, wherein the rim contacts the top surface of the optoelectronic element so that the optoelectronic material is within the region defined by the rim, and

15 (c) an adhesive located in a channel defined by (i) the bottom surface of the lid between the outer edge and the rim, (ii) the rim, and (iii) the top surface of the optoelectronic element not within the region circumscribed by the rim.

2. The device of Claim 1 wherein the optoelectronic material is a luminescent organic compound.

20 3. The device of Claim 2 wherein the luminescent organic compound is a polymer.

4. The device of any one of Claims 1-3 wherein the optoelectronic material conducts current when exposed to light.

5. The device of any one of Claims 1-3 wherein the optoelectronic material is sandwiched between the anode and the cathode.

25 6. The device of any one of Claims 1-3 wherein the cathode comprises a low work function metal.

7. The device of any one of Claims 1-3 wherein there is a thin layer of a getter material on the bottom surface of the lid.

30 8. The device of any one of Claims 1-3 wherein there is a thin film of an inorganic, insulating material on the cathode.

9. The device of any one of Claims 1-3 wherein the lid comprises a material selected from glass, quartz, ceramic, rigid polymers having a coating of a barrier material, aluminum, and stainless steel.

5 10. The device of any one of Claims 1-3 wherein the rim comprises a material selected from glass, quartz, ceramic, or rigid polymers having a coating of a barrier material, aluminum, and stainless steel, with the proviso that if the material is electrically conducting, the portion of the rim that contacts the element is coated with an electrically insulating material.

10 11. The device of any one of Claims 1-3 wherein the adhesive is selected from UV-curable adhesive and two-component epoxy adhesives.

1/3

Figure 1A

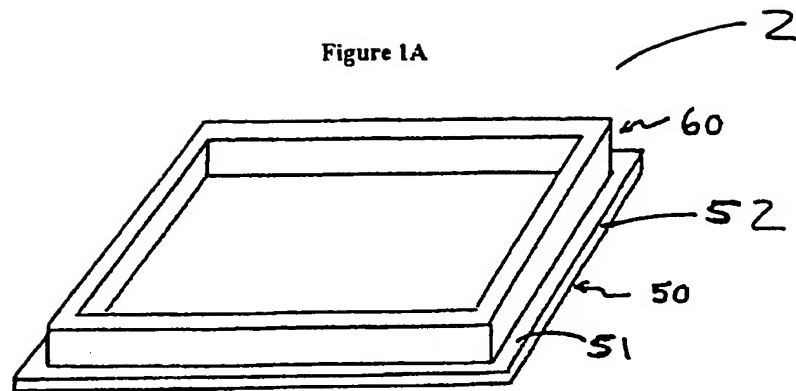
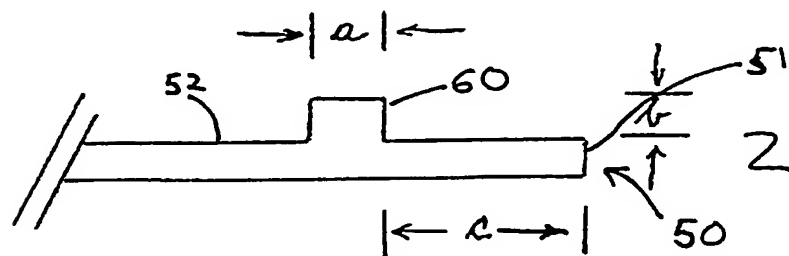


Figure 1B



2/3

Figure 2

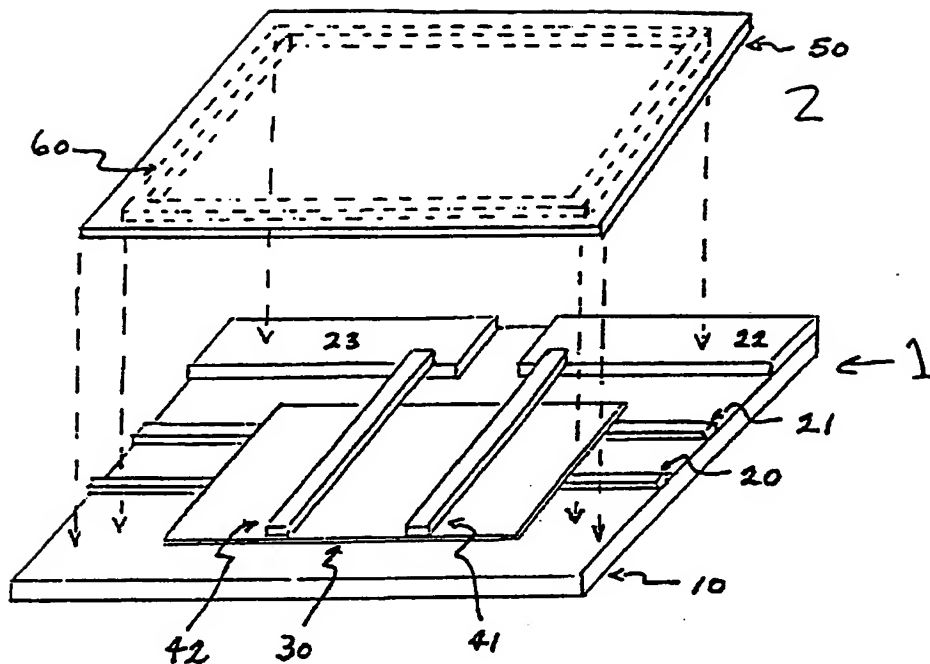
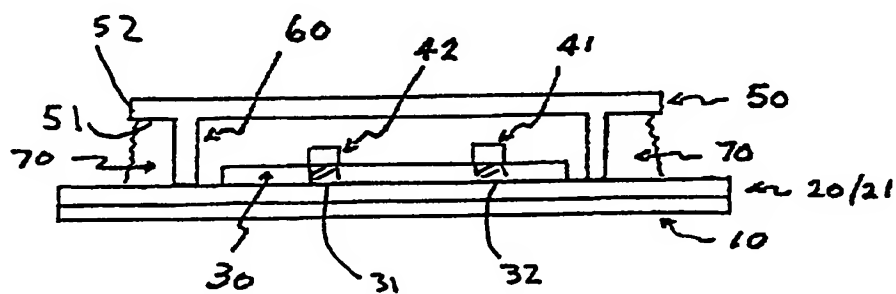
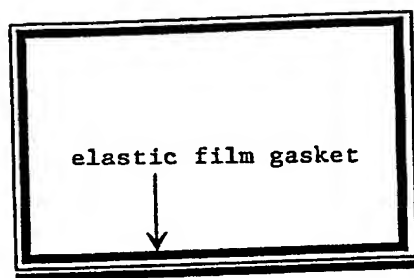


Figure 3



3/3

Figure 4



Glass sheet

INTERNATIONAL SEARCH REPORT

In. .ational Application No

PCT/US 00/10392

A. CLASSIFICATION OF SUBJECT MATTER

IPC 7 H01L51/20

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 7 H01L H05B

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, PAJ, WPI Data

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
E	EP 1 021 070 A (TDK CORP) 19 July 2000 (2000-07-19) the whole document	1,2,5,6, 8,9,11
A	PATENT ABSTRACTS OF JAPAN vol. 017, no. 424 (E-1410), 6 August 1993 (1993-08-06) -& JP 05 089958 A (KOMATSU LTD), 9 April 1993 (1993-04-09) abstract; figures	1,5,7
A	EP 0 884 930 A (IDEMITSU KOSAN CO) 16 December 1998 (1998-12-16) abstract	1,2,5-7, 9,11
	--- -/--	

☒ Further documents are listed in the continuation of box C.

☒ Patent family members are listed in annex.

* Special categories of cited documents :

"A" document defining the general state of the art which is not considered to be of particular relevance

"E" earlier document but published on or after the international filing date

"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)

"O" document referring to an oral disclosure, use, exhibition or other means

"P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.

"&" document member of the same patent family

Date of the actual completion of the international search

21 September 2000

Date of mailing of the international search report

28/09/2000

Name and mailing address of the ISA

European Patent Office, P.B. 5818 Patentlaan 2
NL - 2280 HV Rijswijk
Tel. (+31-70) 340-2040, Tx. 31 651 epo nl,
Fax: (+31-70) 340-3016

Authorized officer

De Laere, A

INTERNATIONAL SEARCH REPORT

Int. Application No

PCT/US 00/10392

C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	EP 0 776 147 A (PIONEER ELECTRONIC CORP ;PIONEER ELECTRONIC TOHOKU (JP)) 28 May 1997 (1997-05-28) figures ---	1,2,5,7
A	EP 0 859 539 A (TDK CORP) 19 August 1998 (1998-08-19) abstract page 7, line 13,14 -----	1,2,5,6, 8,9,11

INTERNATIONAL SEARCH REPORT

Information on patent family members

International Application No

PCT/US 00/10392

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
EP 1021070 A	19-07-2000	JP 2000208252 A	28-07-2000
JP 05089958 A	09-04-1993	NONE	
EP 0884930 A	16-12-1998	WO 9731508 A	28-08-1997
EP 0776147 A	28-05-1997	JP 9148066 A	06-06-1997
		US 5882761 A	16-03-1999
EP 0859539 A	19-08-1998	JP 10233283 A	02-09-1998
		JP 10241858 A	11-09-1998
		US 6049167 A	11-04-2000